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PTO/SB/08A (08-00)

Substitute for form 1449A/PTO		<i>Complete if Known</i>	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>		Application Number	09/844,672
		Filing Date	April 30, 2001
		First Named Inventor	THEKKATH
		Group Art Unit	2193 2191
		Examiner Name	Khatri, Anil
Sheet	1 of 1	Attorney Docket Number	MTEC-005/00US

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	T ⁶
		Office ³	Number ⁴	Kind Code ⁵ (if known)			

Examiner Signature		Date Considered	2/10/86
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1 Unique citation designation number.

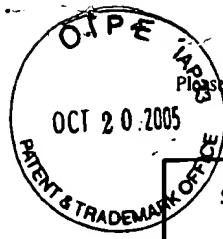
² See attached Kinds of U.S. Patent Documents.

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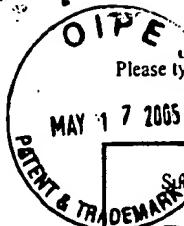
OTHER - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
✓	D1.	F. Chow et al., "Engineering a RISC Compiler System," <i>IEEE COMP-CON</i> , March 1986, pp. 132-137.	
✓	D2.	M.D. Smith, "Tracing with Pixie," <i>Technical Report CSL-TR-91-497</i> , Stanford University, Computer Systems Laboratory, November 1991, pp. 1-29.	
✓	D3.	ATOM Reference Manual, <i>Digital Equipment Corporation</i> , Massachusetts, December 1993, pp. 1-32.	
✓	D4.	A. Srivastava et al., "ATOM: A System for Building Customized Program Analysis Tools," <i>WRL, Research Report 94/2</i> , Digital Equipment Corporation, Massachusetts, March 1994, pp. 1-23.	
✓	D5.	ATOM User Manual, Digital Equipment Corporation, March 1994, pp. 1-28.	
✓	D6.	B. Cmelik et al., "Shade: A Fast Instruction-Set Simulator for Execution Profiling," <i>Proceedings of the 1994 ACM SIGMETRICS Conference</i> , SIGMETRICS, California, May 1994, pp. 128-137.	
✓	D7.	MIPS Technologies; "MIPS64 5K™ Processor Core Integrator's Guide;" [Document No. MD00056]; <i>MIPS Technologies, Inc.</i> ; Rev. 1.2; August 11, 2000; pp. I-II, 1-82.	
✓	D8.	MIPS Technologies; "MIPS64™ 5K™ Processor Core Family Integrator's Guide;" [Document No. MD00106]; ©1999-2001 <i>MIPS Technologies, Inc.</i> ; Rev. 02.00; January 15, 2001; pp. I-VIII, 1-75.	
✓	D9.	MIPS Technologies; "Core Processor Interface Specification;" [Document No. MD00068]; <i>MIPS Technologies, Inc.</i> ; Rev. 1.11; March 30, 2001; pp. 1-26.	

Examiner Signature		Date Considered	210106
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OTHER - NON PATENT LITERATURE DOCUMENTS						
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.				
✓	D1	Susan J. EGGERIS, et al., "Techniques for Efficient Inline Tracing on a Shared-Memory Multiprocessor," University of Washington, 1990 ACM, pp. 37-47.				
✓	D2	Richard A. UHLIG, et al., "Trace-Driven Memory Simulation: A Survey," Intel Microcomputer Research Lab; University of Michigan, <i>ACM Computing Surveys</i> , Vol. 29, No. 2, June 1997, pp. 128-170.				
✓	D3	Eric ROTENBERG, et al., "Trace Processors," University of Wisconsin, 1997 <i>IEEE Service Center</i> , 12 pp.				
✓	D4	E.N. ELNOZAHY, "Address Trace Compression Through Loop Detection and Reduction," ©1999 ACM I-58113-083, pp. 214, 215.				
✓	D5	Darren JONES, "Opal Coprocessor Interface," <i>MIP Proprietary/Confidential</i> , June 4, 1999, pp. 1-18.				
✓	D6	Darren JONES, <i>MIPSS64™ SKC™ Processor Cores User's Manual</i> , Rev. 1.0, July 4, 1999, pp. 6-1 to 6-26.				
✓	D7	<i>MIPSS64™ SKC™ Processor Cores User's Manual</i> , Rev. 1.0.1., 1999, pp. 247-276.				

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